

Using FPGAs in Embedded Designs

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Now a days FPGAs are more and more used within embedded designs. The system architect decides which part of the design will be realized in hardware or software. To make the right choice the system architect must have indebt knowledge about the hardware as well as the software capabilities of an FPGA and in particular the use of a soft-core risc processors

This presentation gives a brief overview about pro's and con's of using the Xilinx MicroBlaze™. soft-core RISC processor in an embedded application. Topics which will be covert are defining the right bus architecture, peripheral selection, the use of hardware acceleration,debugging and software development tools.

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